1c550 U.S. PTO
09/580854
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JC11
625

ISSUE CLASSIFICATION	Class	Subclass
	703	15

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED gh Q.A. CK	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/580854		~~714~~ 703	15	~~2183~~ 2123	~~Tesfaldet~~ Thangavelu

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TITLE
Methods for analyzing integrated circuits and apparatus therefor

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL CLASS	ORIGINAL SUBCLASS	CROSS REFERENCE(S) CLASS	CROSS REFERENCE(S) SUBCLASS (ONE SUBCLASS PER BLOCK)
703	15	703	14
INTERNATIONAL CLASSIFICATION		716	6
G06F 17/50			

☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	5	14	1	43	1

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____

☐ The terminal ___ months of this patent have been disclaimed.

(Assistant Examiner) 3/4/04 (Date)

(Primary Examiner) (Date)

(Legal Instruments Examiner) (Date)

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due	Date Paid

ISSUE BATCH NUMBER

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